

Diagram illustrating the internal structure of the integrated circuit 100. The circuit includes a voltage source 14 connected to a read circuit 10 and a functional circuit 18. The read circuit 10 is connected to a data bus 12. The functional circuit 18 is connected to the data bus 12 and has multiple output lines 16, 20, and 20, which are grouped by a bracket labeled I/O.

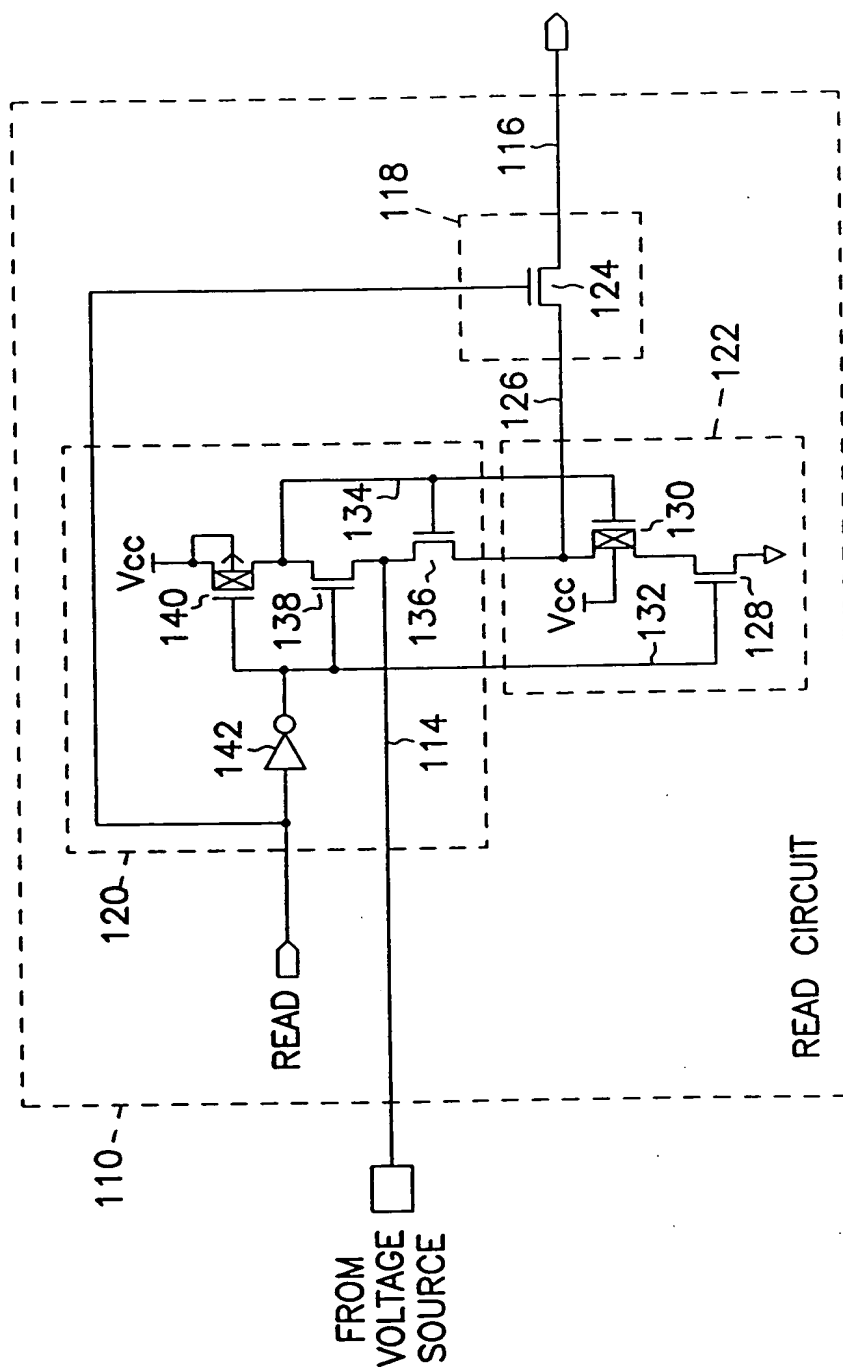


FIG. 2

FIG. 3A

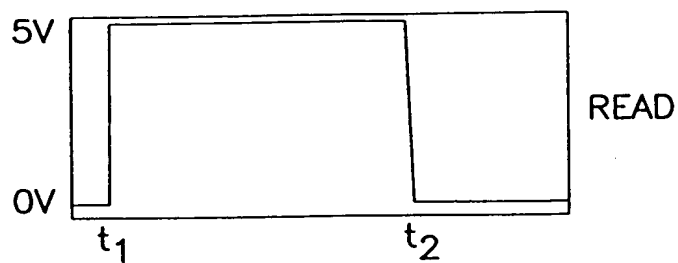


FIG. 3B

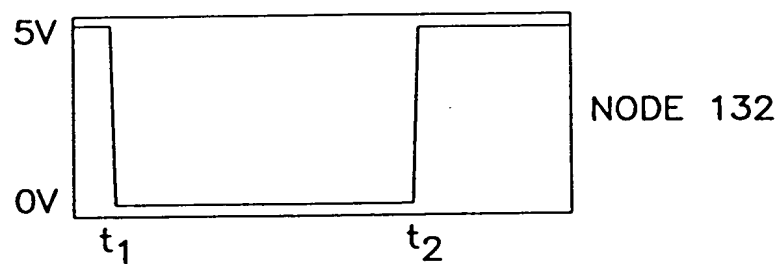


FIG. 3C

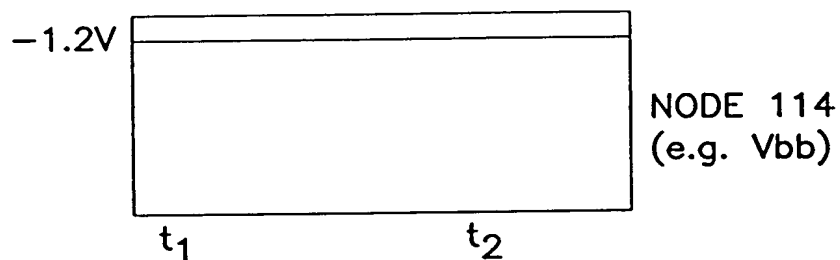


FIG. 3D

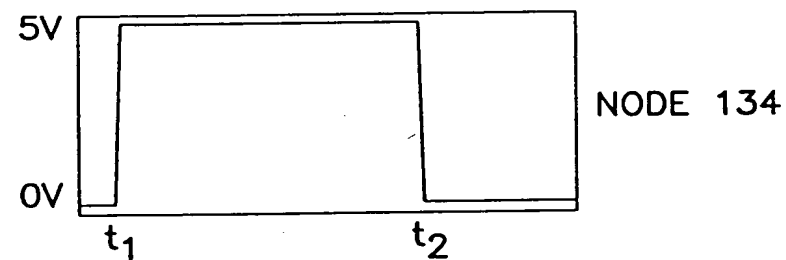


FIG. 3E

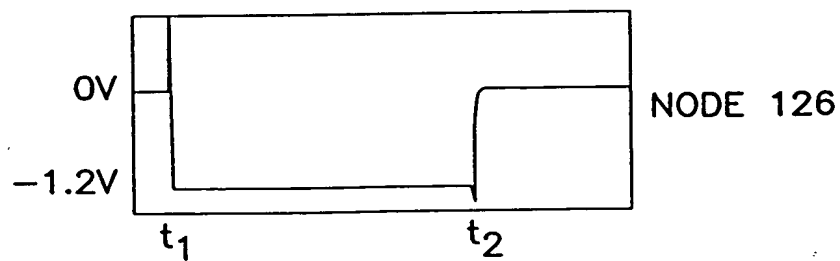
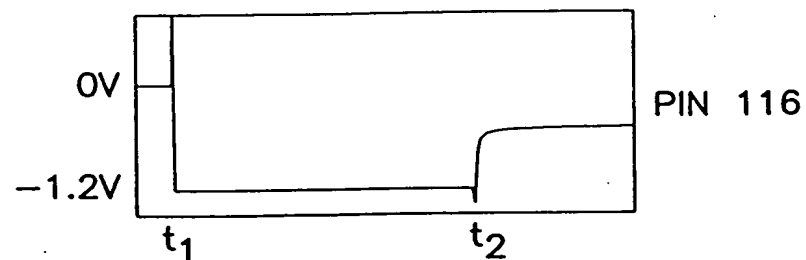
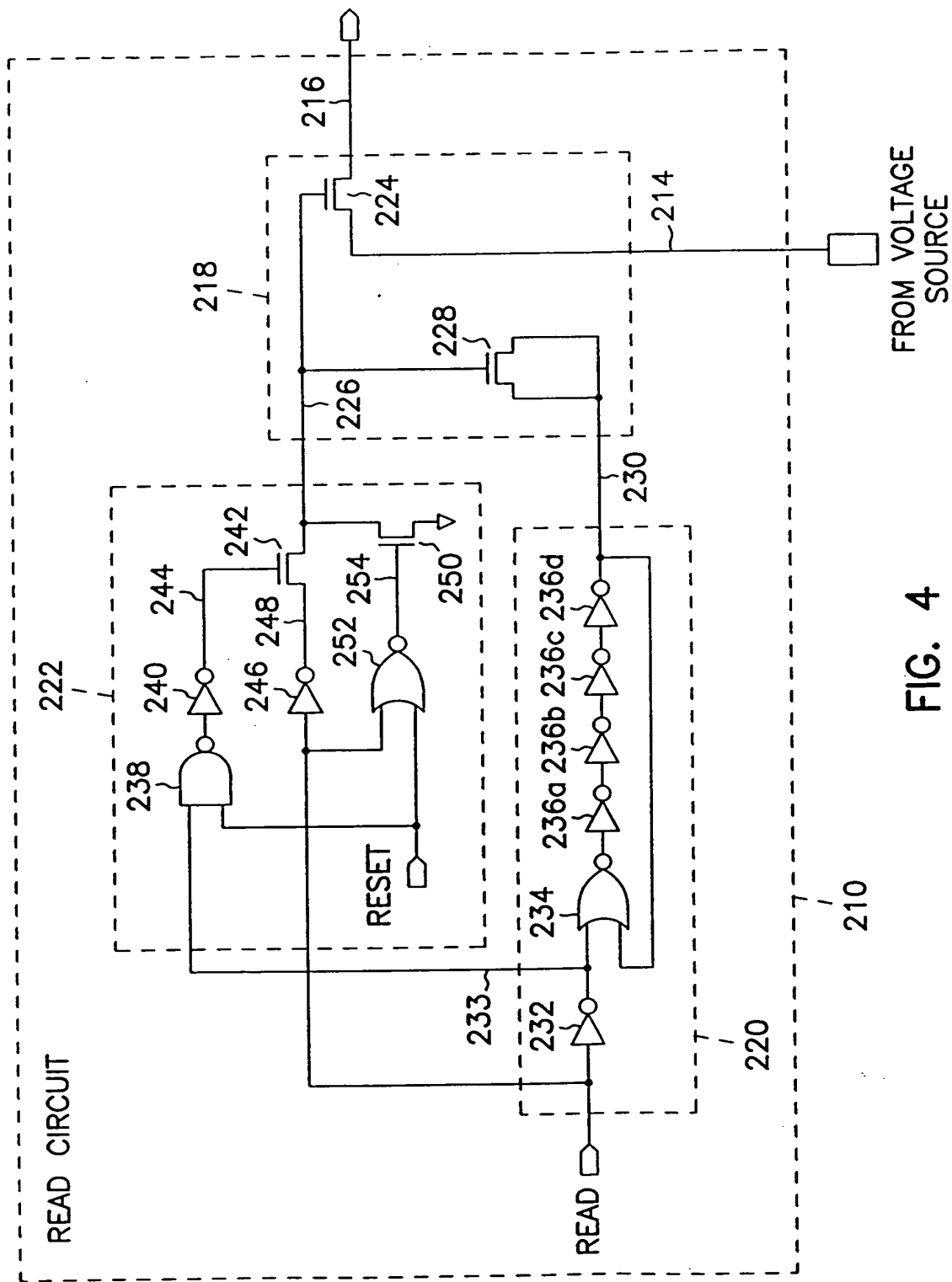


FIG. 3F





FROM VOLTAGE
SOURCE

FIG. 4

